



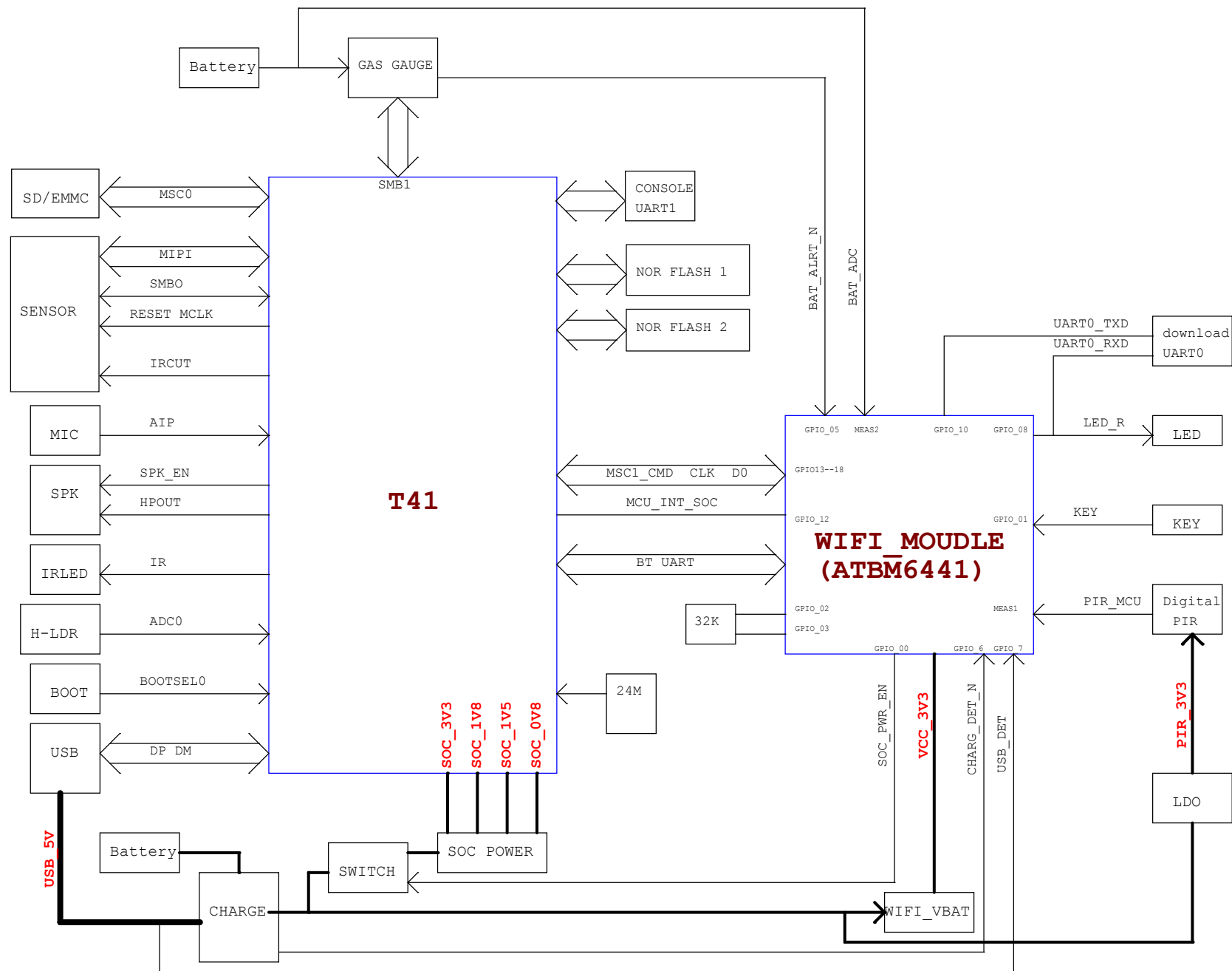
RD_T41_INDUS

Schematic Revision 1.1

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<Core Design>

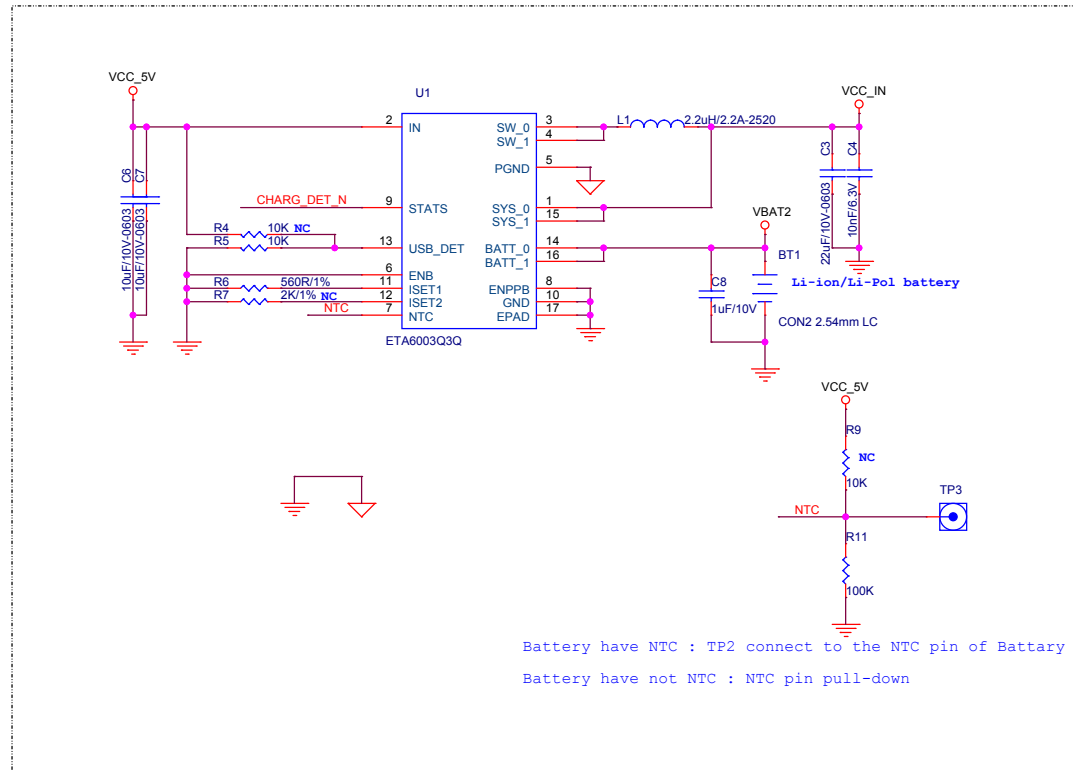
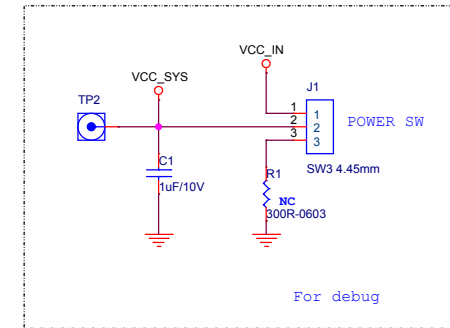
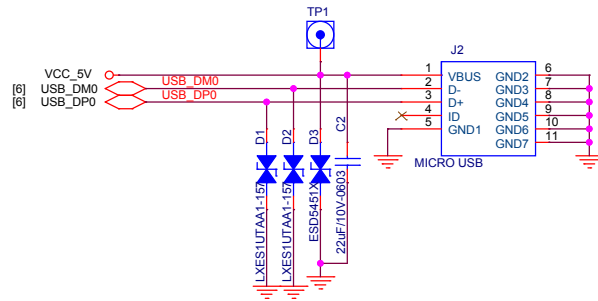
INGENIC SEMICONDUCTOR CO.,LTD		
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RD_T41QFN_INDUS		
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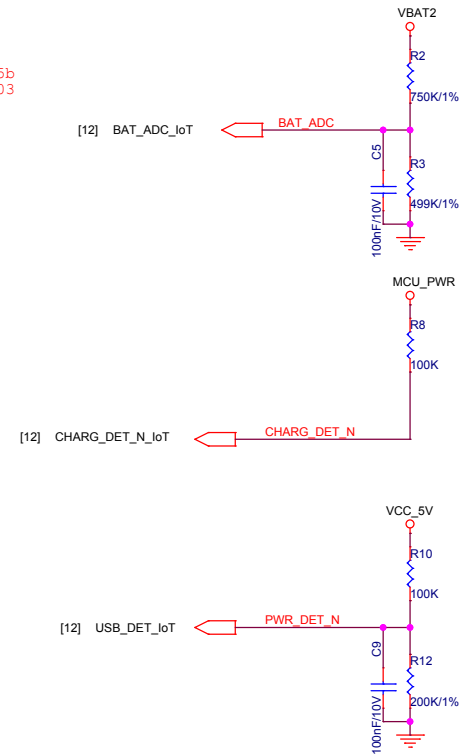
<Core Design>

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POWER-SUPPLY_BAT_USB



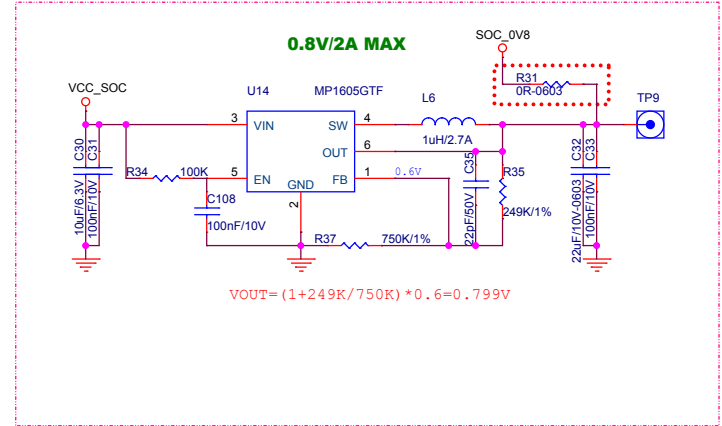
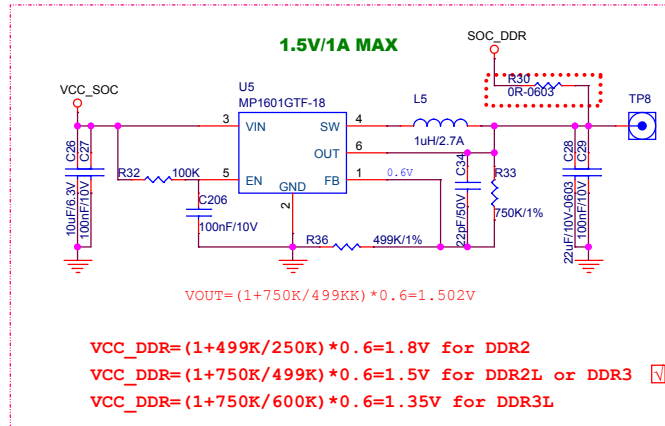
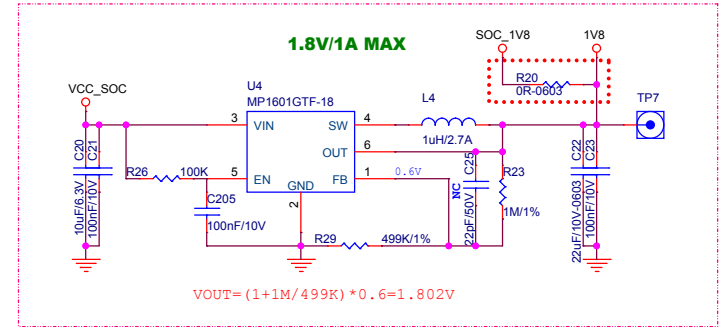
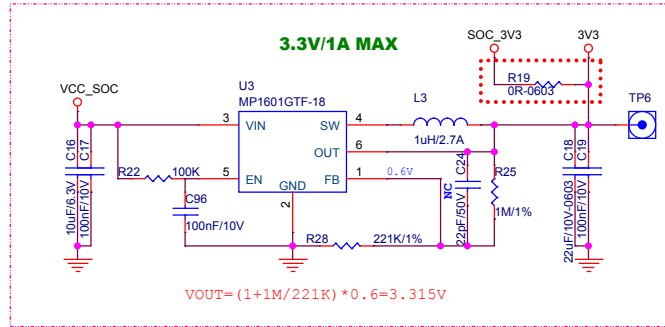
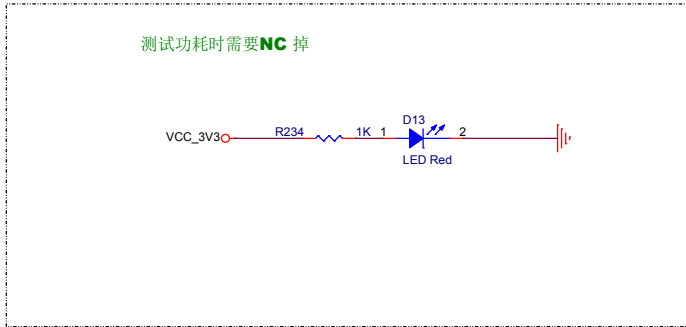
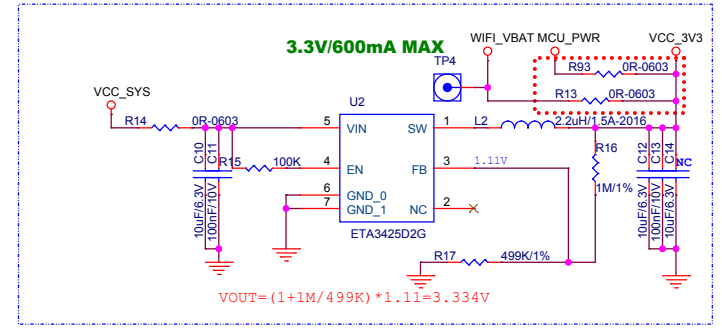
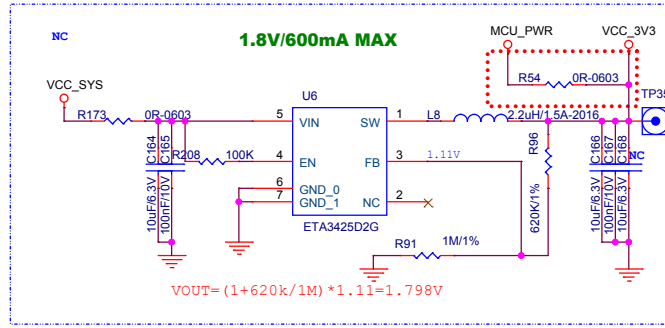
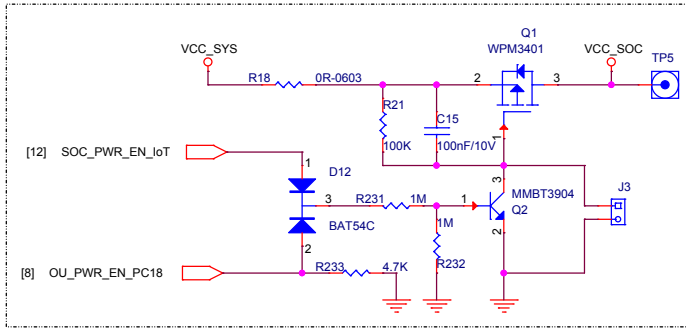
Charger:
1. MP2625b
2. ETA6003
...



<Core Design>

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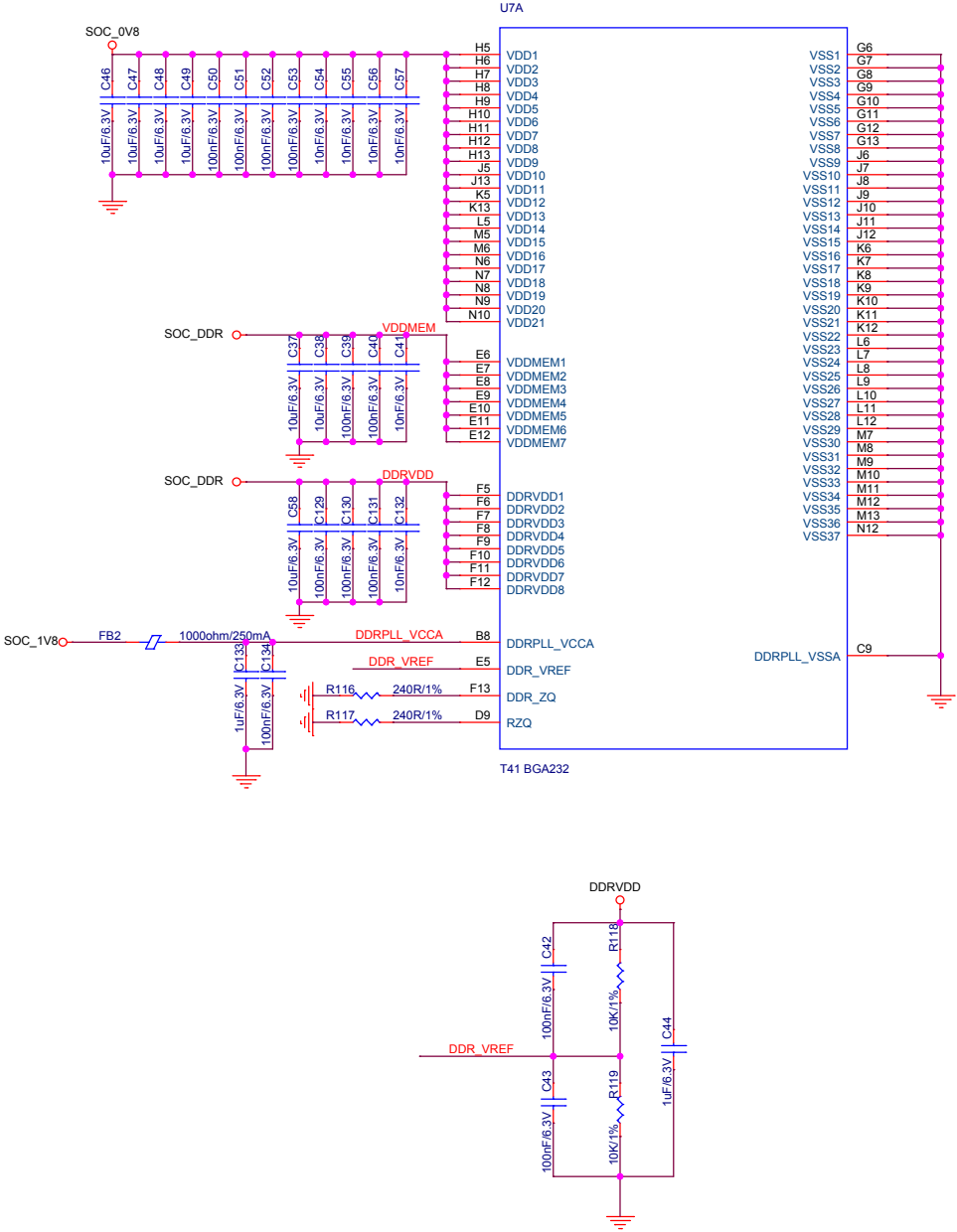
POWER-SUPPLY

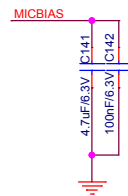
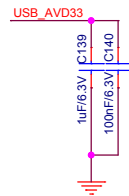
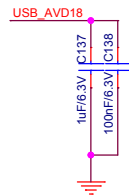
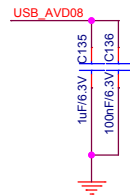
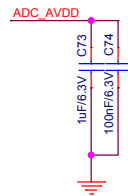
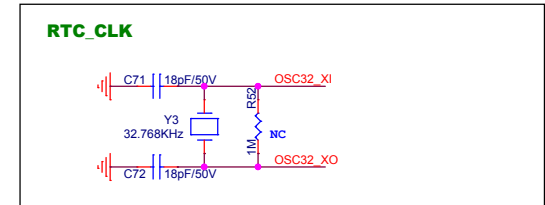


Note: The jumper in..... is used for measuring power consumption and can be removed in the product board.

<Core Design>			
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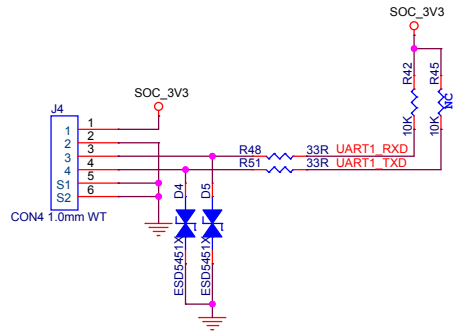
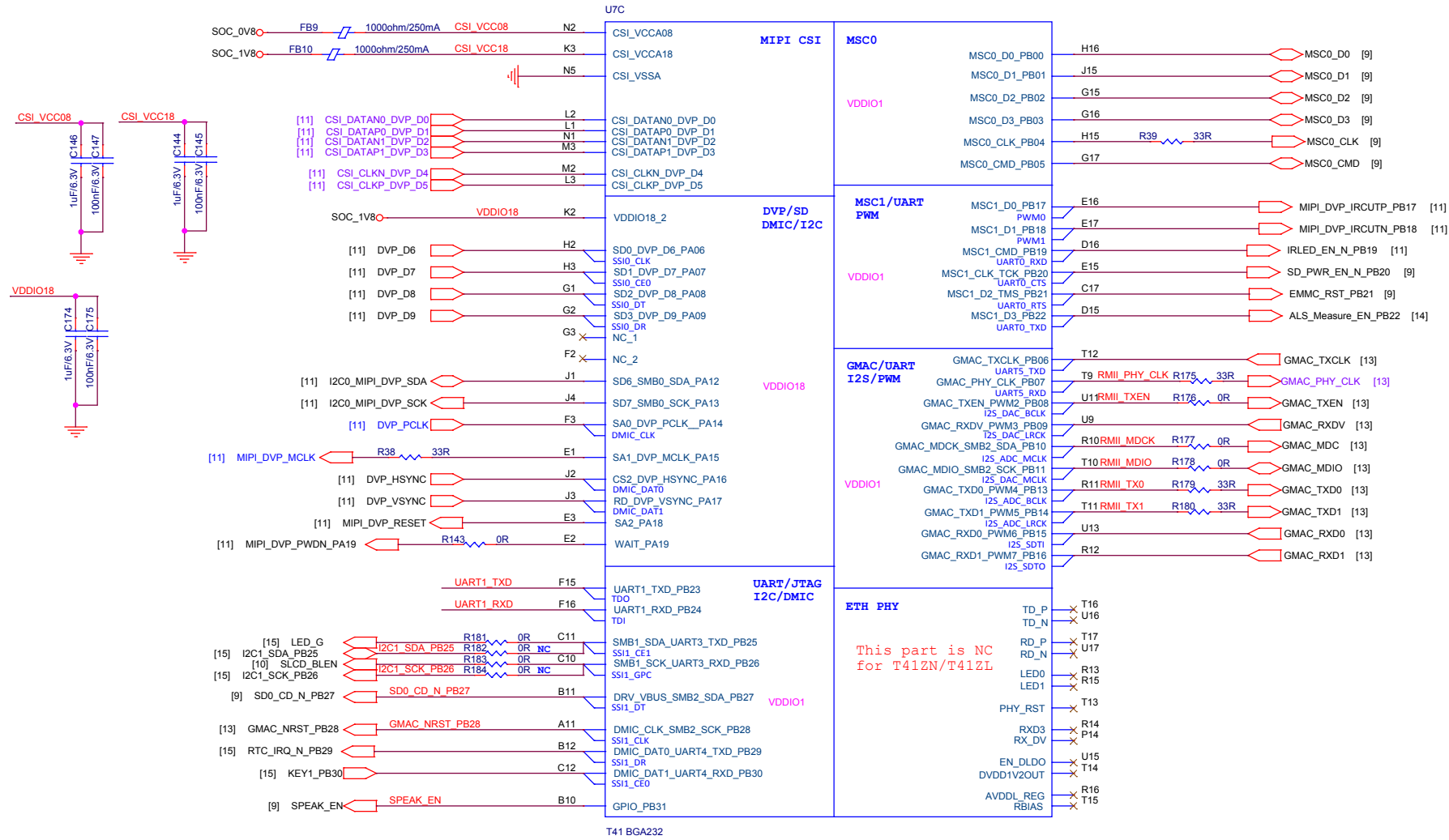
T41-POWER



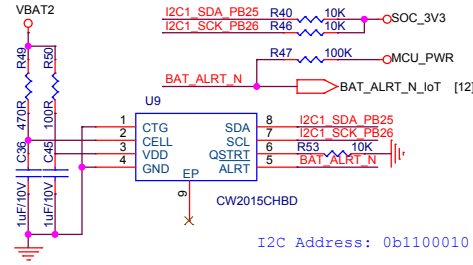
[illegible]

Close to Micbias Pin(P3)

T41_INTERFACE01



GAS GAUGE



<Core Design>

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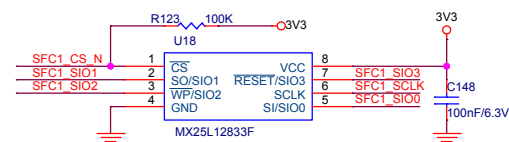
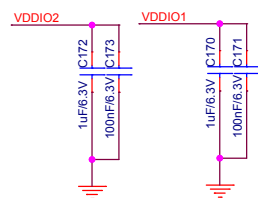
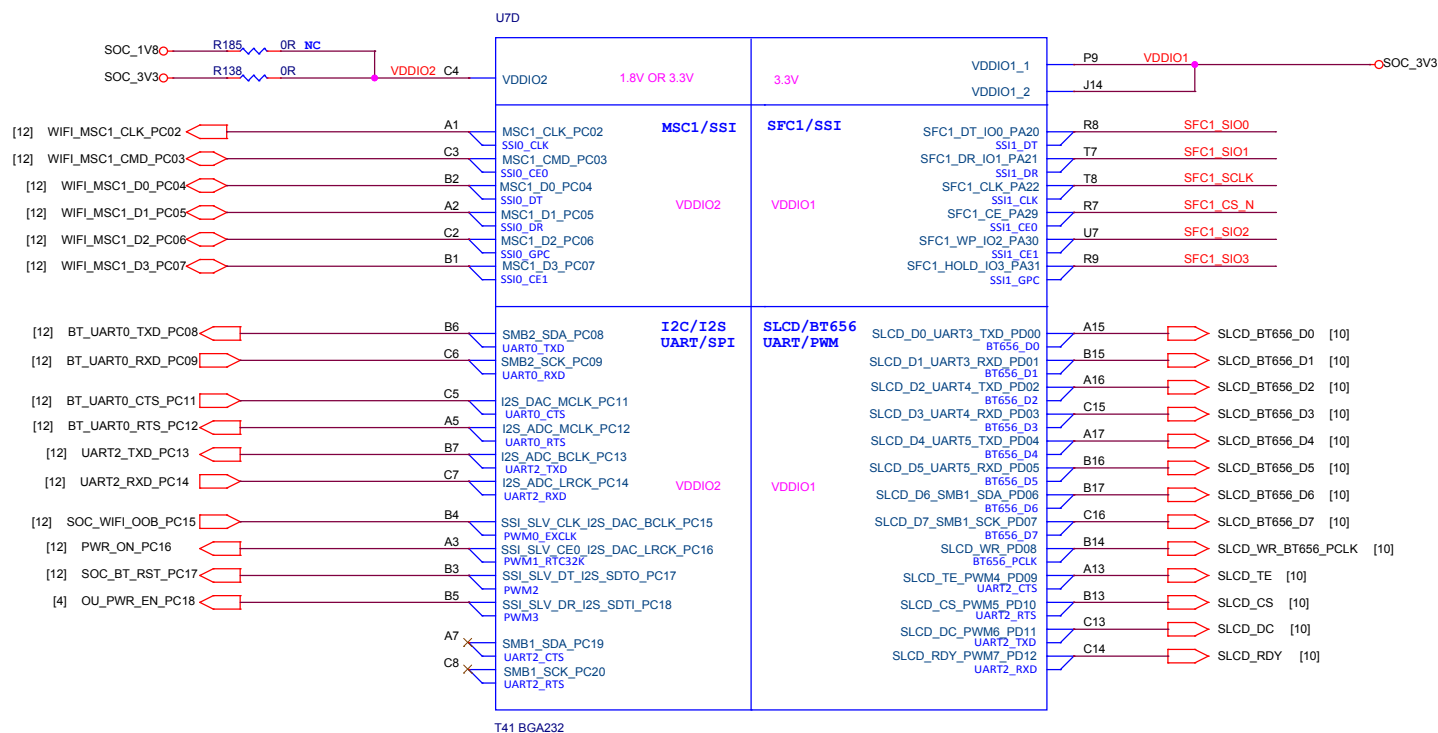
Title RD_T41QFN_INDUS

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T41_INTERFACE02



<Core Design>

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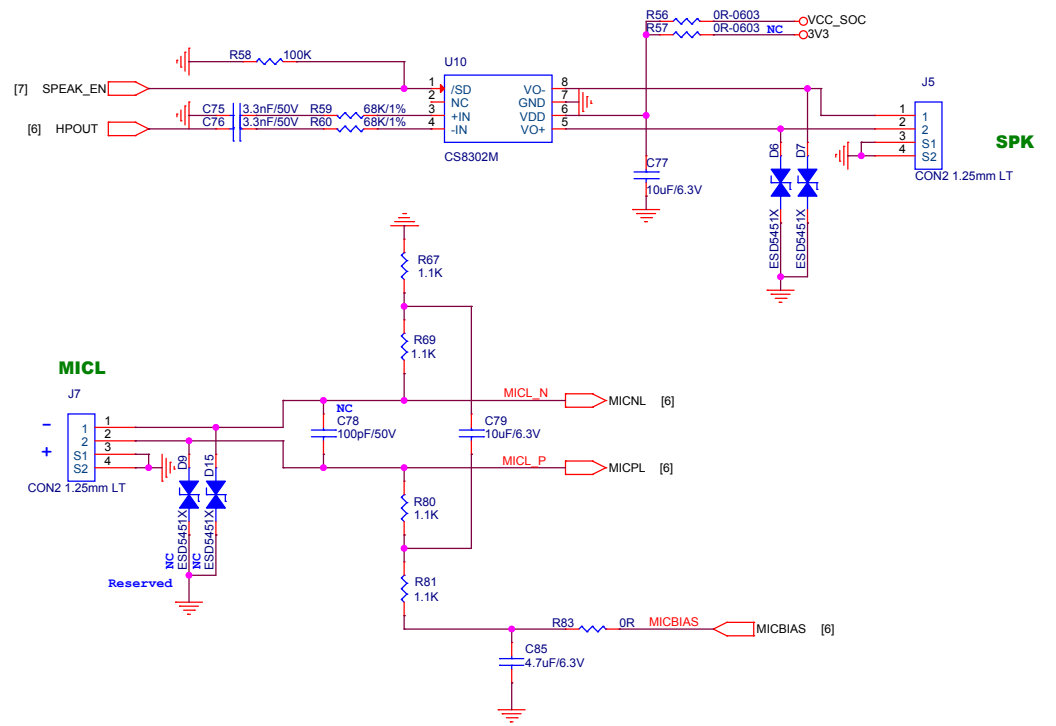
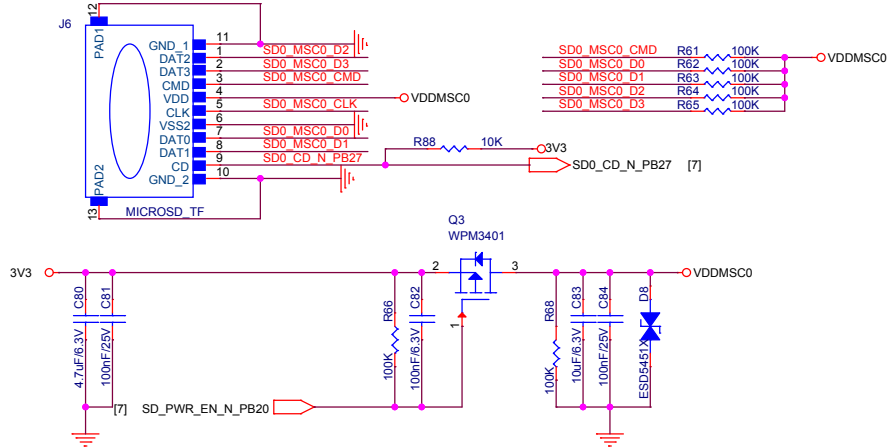
Size	Document Number
A3	T41_INTERFACE02

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-----	-----

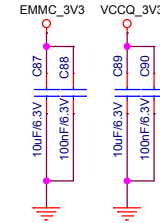
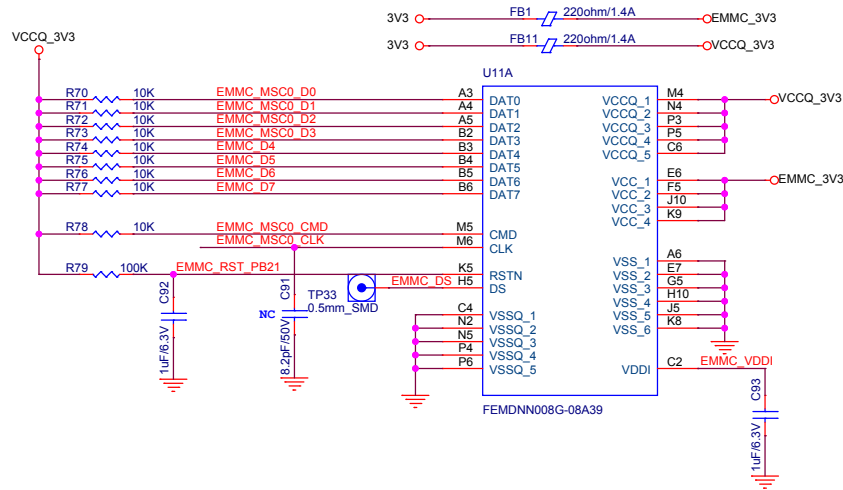
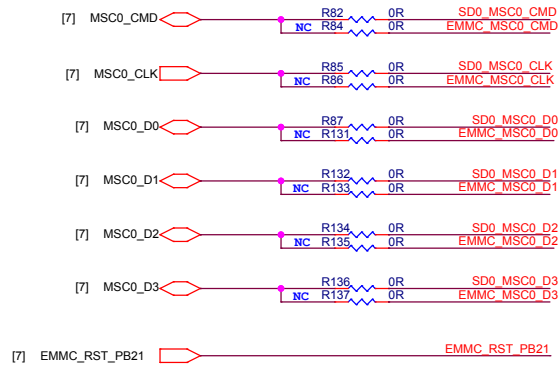
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SD_AUDIO_EMMC

SD CARD



EMMC

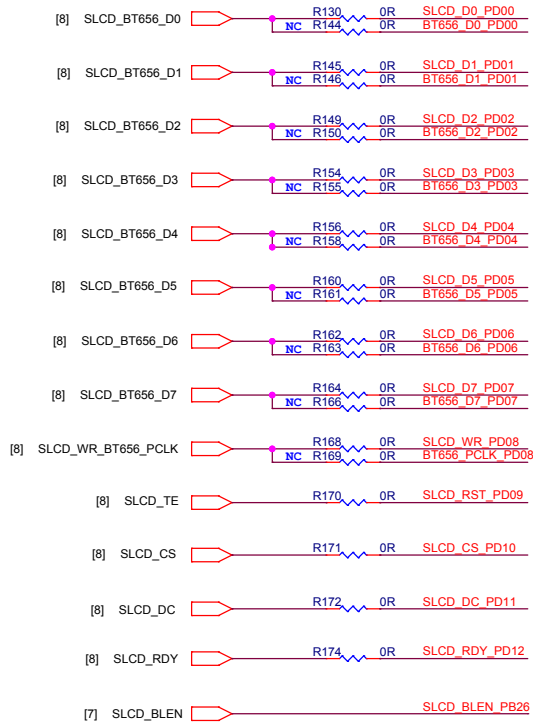
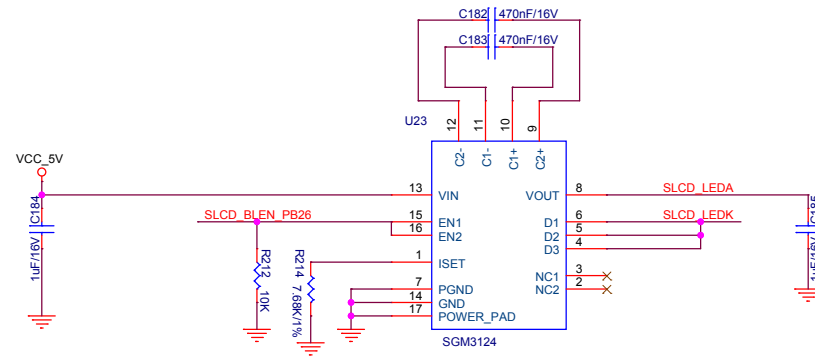


<Core Design>

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SLCD_BT656_OUT

Note: The 0Ω resistance in is used for measuring power consumption and can be removed in the product board.

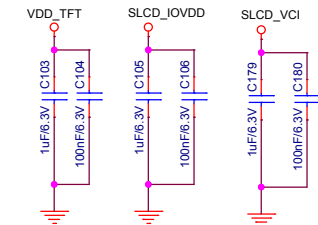
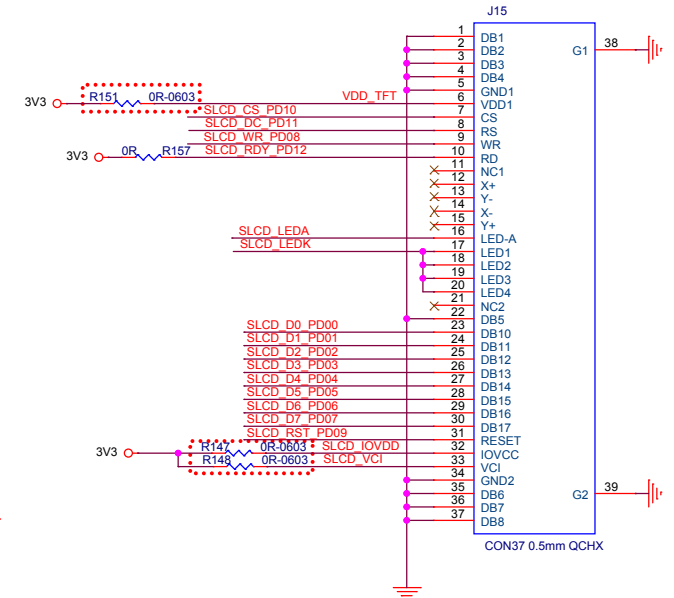
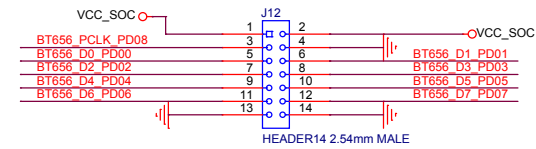
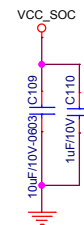
**SLCD_BackLight_Power**

EN1 & EN2

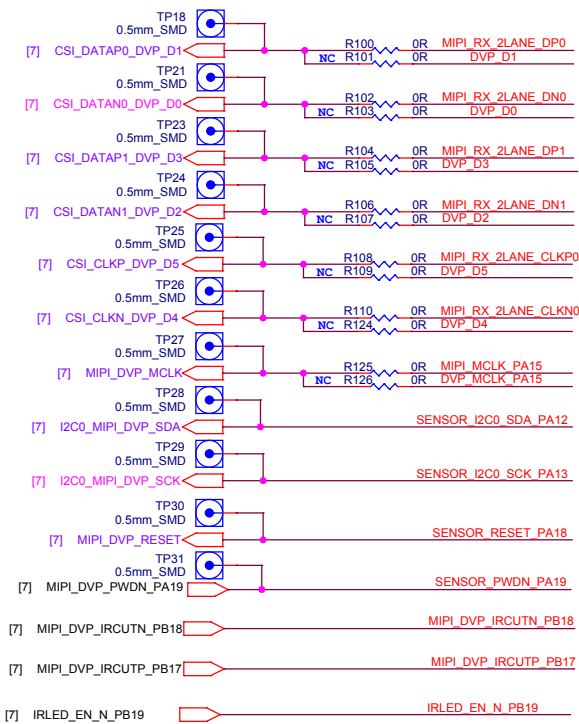
```

0 : BACK LIGHT SHUTDOWN
1 : BACK LIGHT ENABLE
PWM : DIMMING

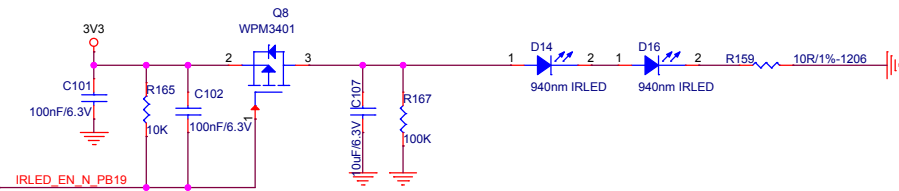
```

$$I(CH) * 3 = 30\text{mA (Max)} * 3$$


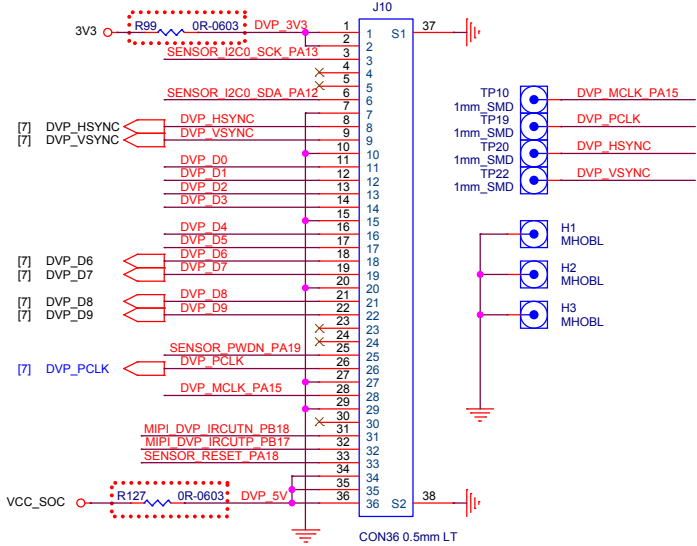
MIPI_DVP_CAMERA



IRLED

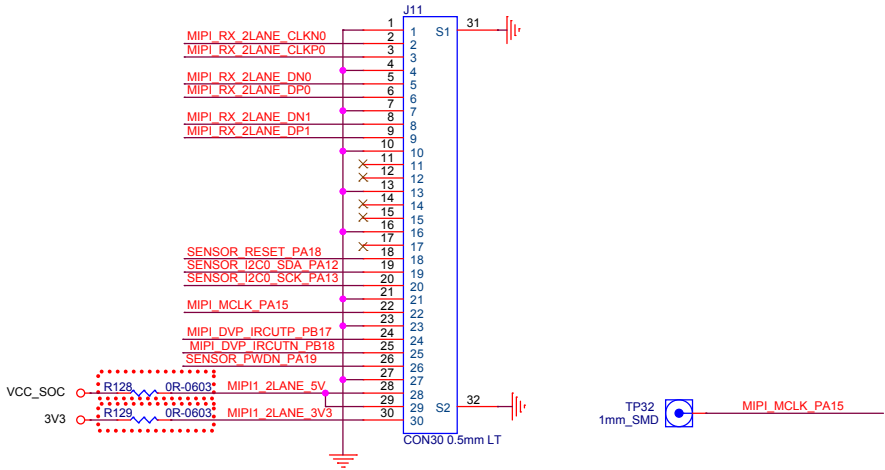


DVP

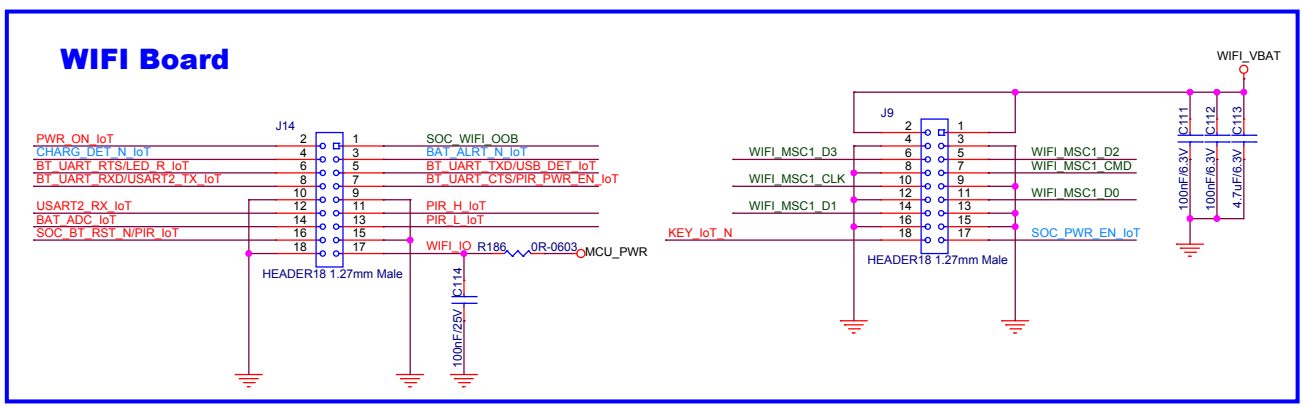
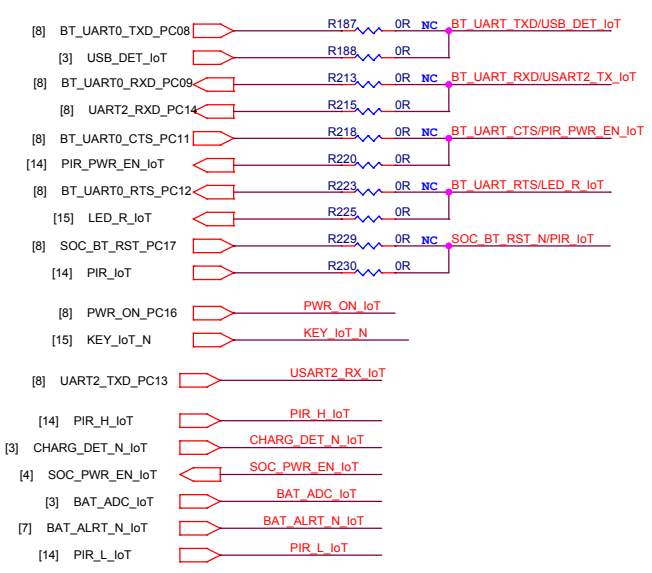
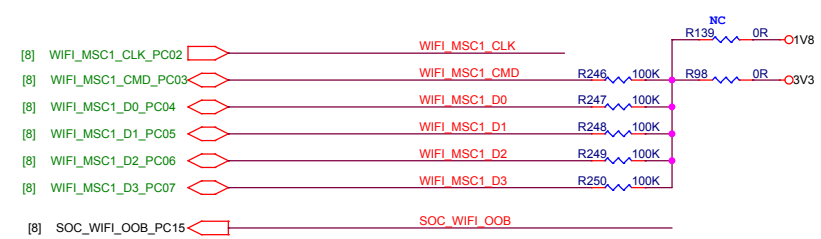


Note: The 0Ω resistance in [] is used for measuring power consumption and can be removed in the product board.

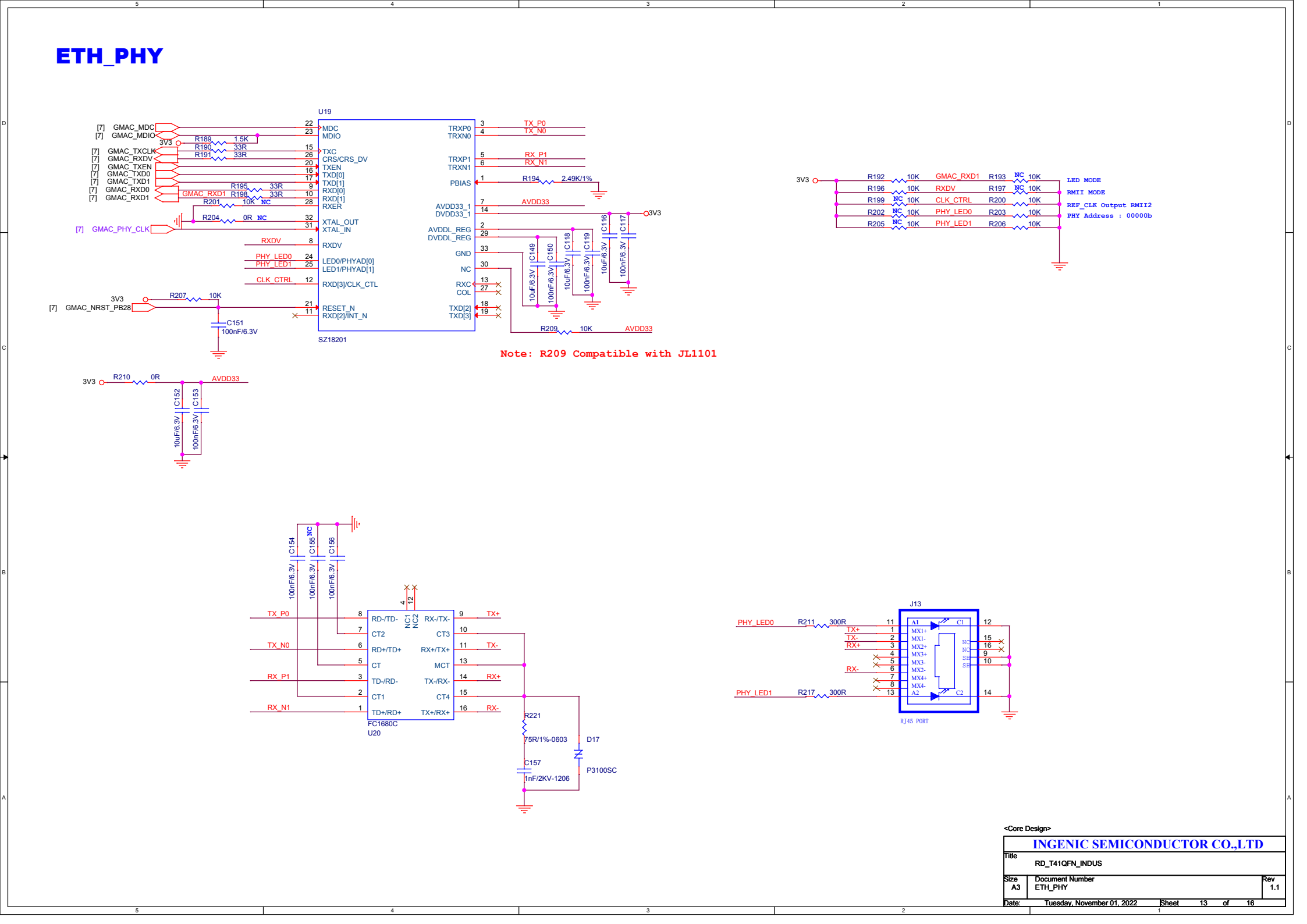
MIPI 2Lane CAMERA



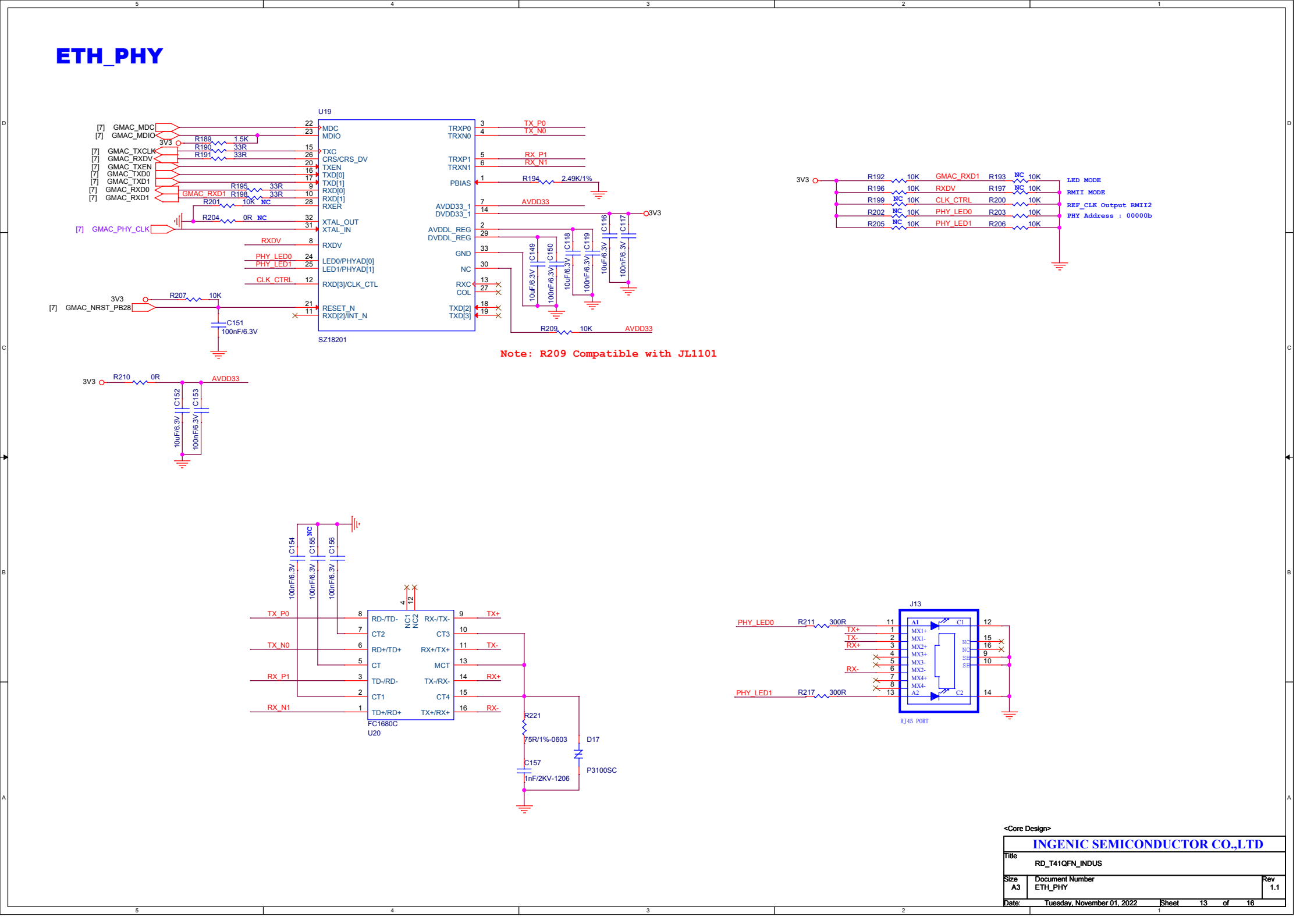
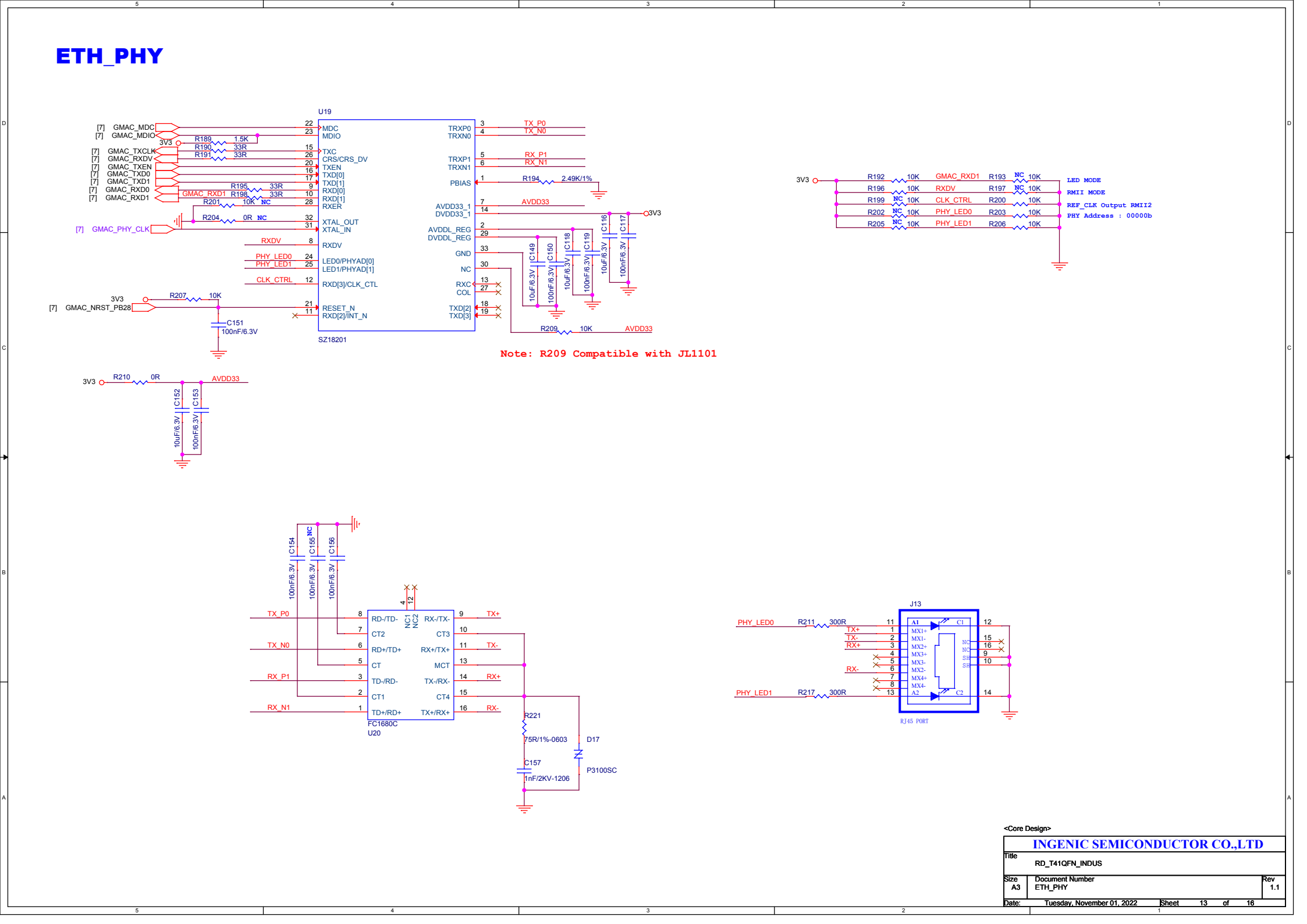
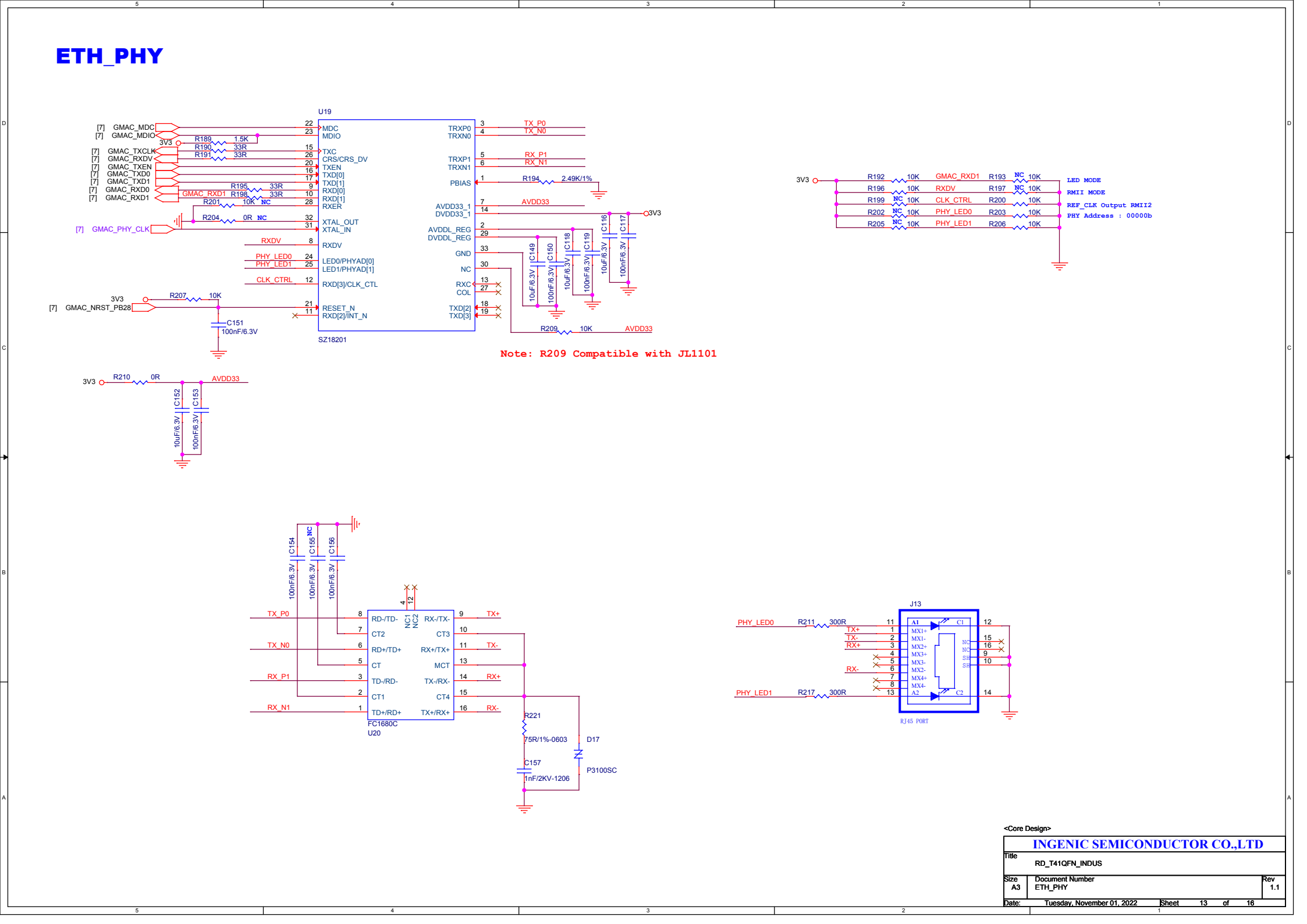
WIFI_MOUDLE(IoT)



ETH_PHY

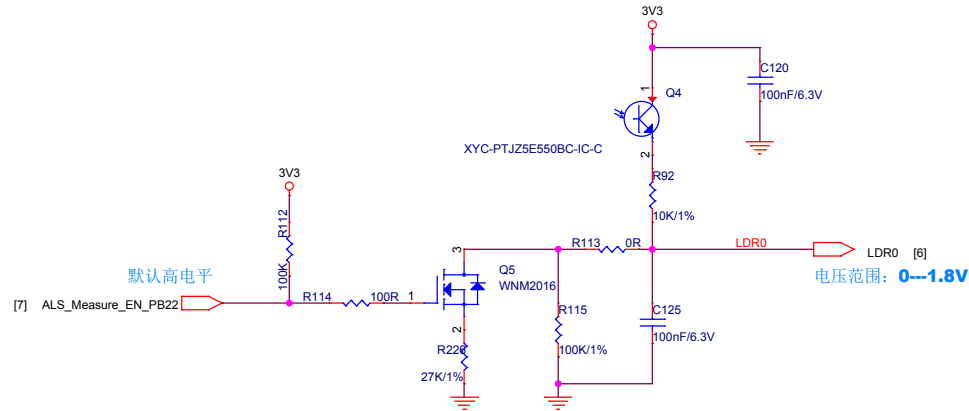
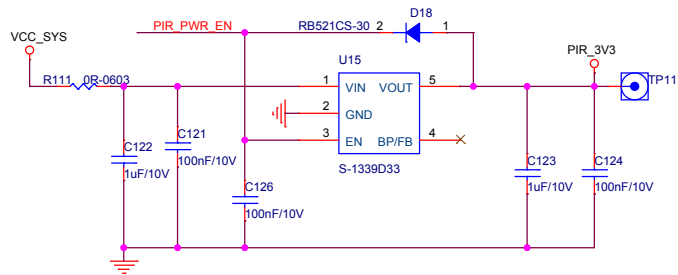


ETH_PHY

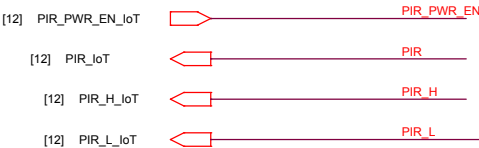


PIR_LDR

PIR Power

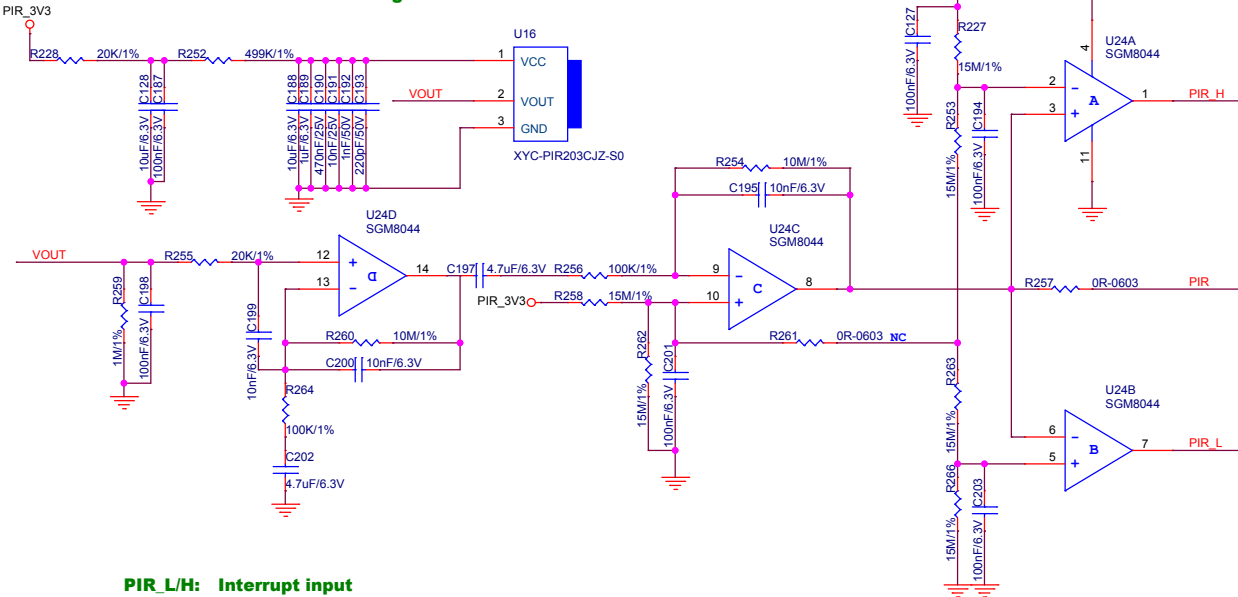


The resistance value is adjusted according to the photodiode



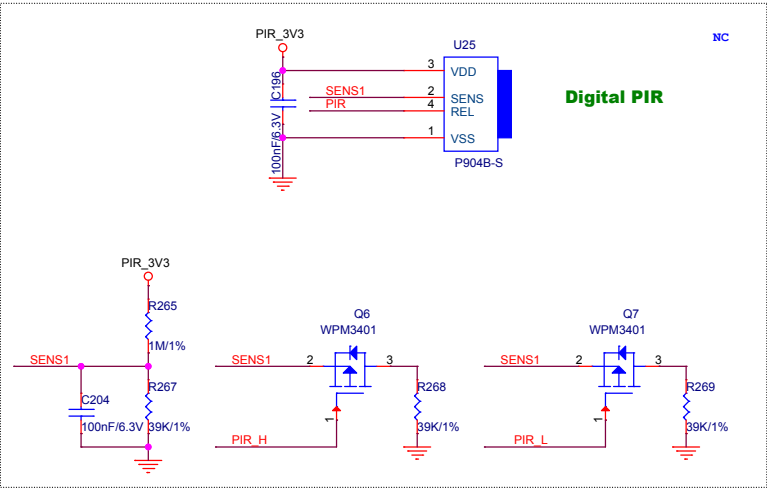
Select analog or digital PIR

Analog PIR



PIR_L/H: Interrupt input
PIR_MCU: Analog signal input

Digital PIR

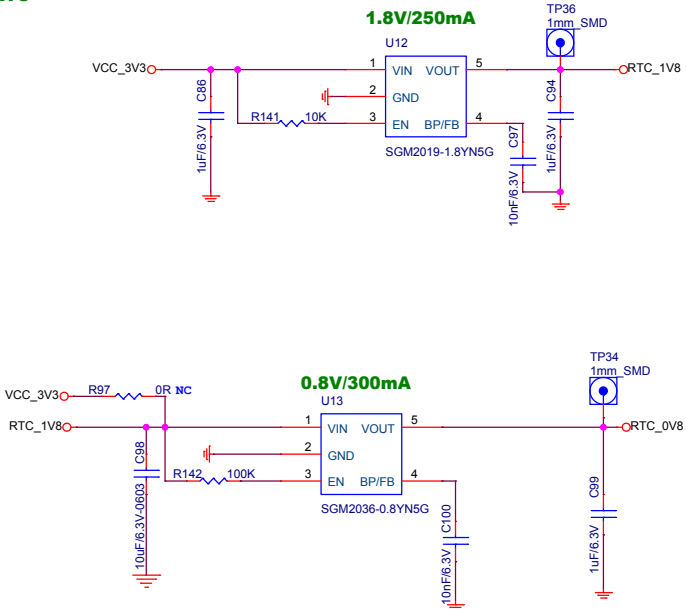


PIR_L/H:
MCU Output, sensitivity can be adjusted dynamically.
The voltage dividing resistance (R153 R156) can be adjusted according to the demand.
Optional Features

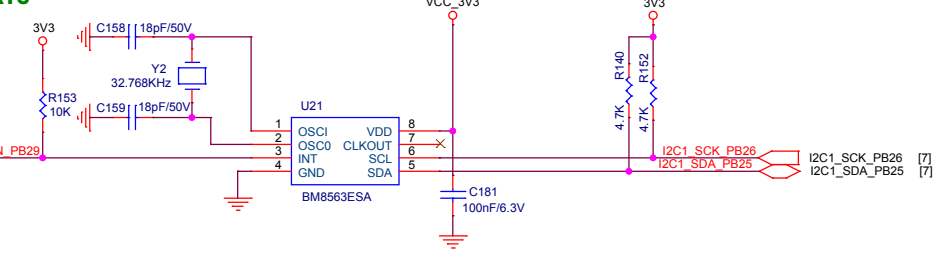
<Core Design>			
INGENIC SEMICONDUCTOR CO.,LTD			
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KEY_LED_RTC_BOOT

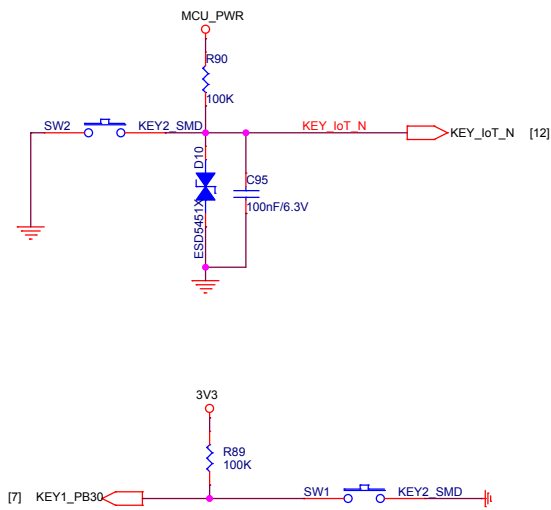
INT RTC



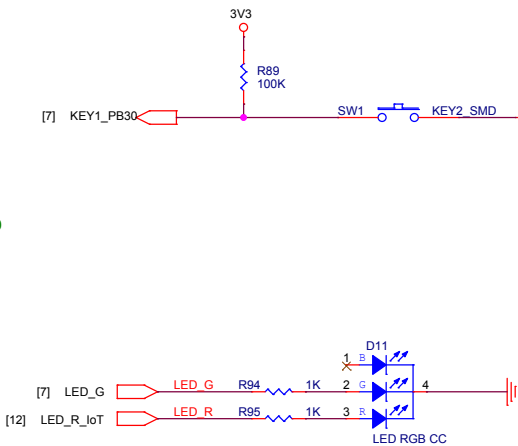
EXT RTC



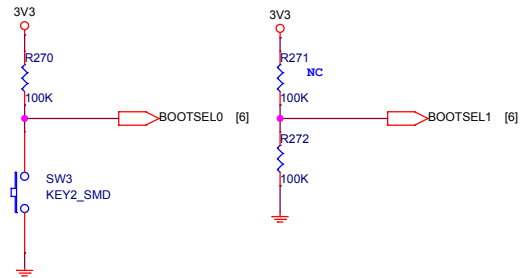
KEY



LED



BOOT



BOOT SEL1	BOOT SEL0	BOOT FROM
0	0	MMC/SD boot@MSC0 (MMC/SD use GPIO Port B, MSC1 use GPIO Port C)
1	0	UART boot @ CS2 (Ymodem@115200/9600)
1	1	USB boot @USB2.0 device, EXTCLK=24MHz
0	1	SFC boot @ CS4 (SPI boot)

Date	Revision	Change
Sep 20 2022	Rev1.0	1. First Revision
Nov 01 2022	Rev1.1	1.T41.G3&F2 Ball Change to NC. 2.ADC Channel Series 4.7R Resistor. 3.Update T41 package